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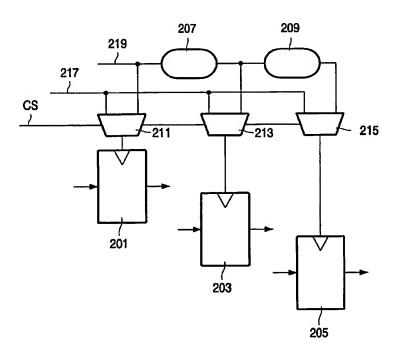
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(54) Title: CONTROLLING POWER CONSUMPTION PEAKS IN ELECTRONIC CIRCUITS



(57) Abstract: An electronic circuit is provided that comprises a plurality of storage elements (101 - 105) arranged for storing of data elements, and a plurality of processing elements. The plurality of processing elements processes the data elements stored in the storage elements. In operation, the points in time at which respective storage elements load their data elements are mutually different in order to meet a maximum allowable value of the power consumption peaks.





FR, GB, GR, HU, IE, IS, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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